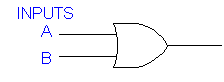
**Quiz 2 : Digital Logic Design 750231 07/04/2015**

**Name : Student #:**

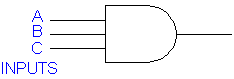
**Choose the correct answers in the following questions:**

1. What is the output X if both inputs A and B are 0?

X

* 1. **0**
  2. 1
  3. A **'**
  4. B **'**

1. For a three inputs (A,B C) OR gate, what inputs are needed if output=0?
   1. A=0, B=0, C=1
   2. A=0, B=1, C=0
   3. **A=0, B=0,C=0**
   4. A=1, B=1, C=1
2. What is the output X if input A=1, B=0 and C=1?



* 1. **0**
  2. 1
  3. A
  4. C

1. **Half adder circuit is \_\_\_\_\_\_.**

a. Half of an AND gate

**b. A circuit to add two bits together**

c. Half of a NAND gate

d. none of above

1. **Half adder consists of. …… and …..Gates**

**a. EX-OR, AND**

b. EX-OR, OR

c. EX-OR, NOT

d. None of the above